	\sim											
oa I/(U Inte	ellident E series										
- J -		37-pin Analog Input	Analog Output	Digital I/O	Counter	E	High	High	Memory	0.11		
_ P(D-SUB 16ch	1ch	4/4	1ch	series	Precisio	Speed	on Board	Complian		and and
Expr	ess	Windows Driver	Linux I	Driver	C-LOG	GER	MATL	AB	LabVIE	w	1	
11MS/	s 16-bit l	Multi-function	• 16M da	to buffor	momory			buffe	(*)		Plan 1	
Analog	j I/O		 A variet 	ty of acce	essories o	can exte	end fun	ctions	<i></i>)			
AIO-1	61601U	E3-PE NEW	The sta	rt/end of :	sampling (can be	perform	ed by	software	commar	nd, 🥄	16ch Multiplexer S
			Input da Eeature	ata compa es softwa	arison or e ire-based	external calibra	tion fur	vel Inp action	ut			Can add 16 single-ei
			•							~		o unerential inputs.
P	21	37-pin Analog Input	Analog Output	Digital I/O	Counter	E	High	Memory			S)	1 Ste
Expr	229	D-SUB TOCH	Ten	4/4		series	Precisio	on Board			100	
		windows Driver	Linux	Driver	C-LOG	GER	MAIL	AB	Labvie	<u>w</u>		
100KS/	/S 16-DIT	Multi-function	• 16M da	ta buffer	memory	(FIFO	or RINC	G buffe	er)			
	61601F		 A variet The state 	ty of acce rt/end of :	essories o sampling (can exte	end fun perform	ed by	software	commar	nd.	16ch Multiplexer S
			Input da	ata compa	arison or e	external	TTL-le	vel Inp	ut	oomina	,	ATCH-16A(PCI)
			Feature	es softwa	re-based	calibra	tion fur	nction				8 differential inputs.
		37-pin Analog Input	Analog Output	Digital I/O	Counter	F	High	Memory		Roll	S)	2
- P(D-SUB 16ch	1ch	4/4	1ch	series	Speed	on Board		compila		
Expr	ess	Windows Driver	Linux I	Driver	C-LOG	GER	MATL	AB	LabVIE	w	C.Y.	Contraction of the second
1MS/s	12-bit M	lulti-function	• 16M da	ita buffer	memory	(FIFO (or BING	3 buffe	er)		199	
Analog	g I/O		 A variet 	ty of acce	essories o	can exte	end fun	ctions	,			
AIO-1	21601U	E3-PE	The sta Input de	rt/end of ata compa	sampling (arison or e	can be	perform	ied by : vel Inn	software ut	commar	nd,	16ch Multiplexer S ATUH-16A(PCI)
			 Feature 	es softwa	ire-based	calibra	tion fur	nction	ut			Can add 16 single-ei 8 differential inputs.
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		D-SUB 16ch	1ch		Counter 1ch	E	Memory on Board			Complian		C. Star
Expr	CI 'ess	D-SUB 16ch	1ch	4/4	Counter	E series	Memory on Board	AB	LabVIE	Compliant		
	ess /s 12-bit	Windows Driver	1ch Linux I	4/4 Driver	Counter 1ch C-LOGO	E series GER	Memory on Board MATL	AB	LabVIE	W M		
Expr 100kS/	/s 12-bit	Windows Driver Multi-function	1ch Linux I • 16M da	4/4 Driver	Counter 1ch C-LOGO memory	E series GER (FIFO (Memory on Board MATL or RINC	AB	LabVIE er)	W KOH		
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Expr 100kS/ Analog AIO-12	CI 'ESS /s 12-bit g I/O 21601E:	AIO-161601UE3	1ch Linux I • 16M da • A variet • The sta Input da • Feature • PE	4/4 Driver ty of acce rt/end of s ata compa es softwa AlO-16	Counter 1ch C-LOG memory essories c sampling arison or e ire-based 1601 E3-P	GER (FIFO of can external calibra	Memory on Board MATL or RINC end fun perform TTL-lev tion fur AIC	AB a buffe ictions ied by vel Inp iction D-1216	LabVIE er) software ut 501UE3-	commar PE	nd,	16ch Multiplexer S ATCH-16A(PCI) Can add 16 single-er 8 differential inputs. 121601E3-PE
Expr 100kS Analog AIO-12	(ess) (s 12-bit 1/0 21601E	D-SUB 16ch Windows Driver Multi-function 3-PE NEW AIO-161601UE3 16 single-ended, 8 difference	1ch Linux I 16M da A variet The sta Input da Feature -PE erential (Supp	4/4 Driver ata buffer ty of acce rt/end of s ata compa es softwa AIO-161 orts up to 3	Counter 1ch C-LOG(rememory essories of sampling of arison or e are-based 1601E3-P 2 single-end	E Series GER (FIFO of can external calibra E ed, 16 diff	Memory on Board MATL or RINC end fun perform TTL-le tion fur AIC ferential ir	AB G buffe actions aed by vel Inp action D-1216 aput with	LabVIE er) software ut so1UE3-	commar PE ultiplexer s	nd, AIO- ub board	16ch Multiplexer S ATCH-16A(PCI) Can add 16 single-er differential inputs. 121601E3-PE
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Expr 100kS, Analog AlO-12	els els	B-SUB 16ch International Stress Internation International Stress Internation International Stress Internatio Stress International Stress Internating Stress Inte	1ch Linux I 16M da A variet The sta Input da Feature -PE erential (Supp	A/4 Driver at a buffer ty of accert/end of stata compa es softwa AIO-161 orts up to 32	Counter 1ch C-LOG rememory essories of sampling of arison or e tre-based 1601E3-P 2 single-ende	E series GER (FIFO of can extor can be external calibra E ed, 16 diff	Memory on Board MATL or RINC end fun perform TTL-let tion fur AIC ierential in 12bi Bipo	AB G buffe actions and by s vel Inp action D-1216 apput with t	LabVIE er) software ut soft UE3- channel m	Comman comman PE 5V:	nd, AlO- ub board Bipola	I fich Multiplexer ATCH-16A(PC) Can add 16 aingle- 8 differential inputs. 121601E3-PE T: ±10V, ±5V, ±2.5V
Expr 100kS/ Analog AlO-12	els Range	D-SUB 16ch Windows Driver Multi-function 3-PE NEW AlO-161601UE3 16 single-ended, 8 diffe 16bit 16bit Bipolar: ±10V, ±5V:Un 25V:Un	1ch Linux I 16M da A varie The sta Input da Feature -PE erential (Supp	4/4 Driver ta buffer ty of accert/end of stata compa as softwa AIO-161 orts up to 32 4.0-+5V (iiu	Counter 1ch C-LOG(r memory essories c sampling (arison or e tre-based 1601E3-P 2 single-ende	E series GER (FIFO of can external calibra E ed, 16 diff	Memory on Board MATL or RINC end fun perform TTL-lev tion fur AIC terential ir 12bi Bipo Unip	AB a buffenctions ned by a vel Inp nction D-1216 nput with t blar: ±100 polar: 0~	LabVIE er) software ut channel m v, ±5V, ±2. +10V, 0-+t	Comman comman PE 5V; ;v	nd, AIO- ub board Bipola Unipol 0~+1;	16ch Multiplexer f Arch-16A(PCI) Can add 16 angle- 8 differential inputs. 121601E3-PE) r: ±10V, ±5V, ±2.5V (arc 0+10V, 0-5V) 25V (iumger or 55V
Expr 100kS, Analog AlO-12	els Range	D-SUB 16ch Windows Driver Multi-function 3-PE NEW AlO-161601UE3 16 single-ended, 8 diffe 16h 16bit Bipolar: ±10V, ±5V;Uni 10V, ±5V;Uni	1 Ch 1 Ch 2 Ch 1 Ch 1 Ch 1 Ch 1 Ch 2	4/4 Driver that buffer ty of acce rt/end of a tata compp as softwa AIO-161 orts up to 30 /, 0~+5V (ju	Counter 1ch C-LOG memory essories of sampling to arison or c tre-based 1601E3-P 2 single-endo	E series GER (FIFO o can external calibra E ed, 16 diff	Memory on Board MATL or RINC end funn TTL-lev tion fur AIC ferential ir 12bi Bipo Unip (jum	AB C buffe ictions led by vel Inp nction D-1216 nput with t t ular: ±10 ¹⁰ polar: 0~~ pper selen	LabVIE er) software ut contucts contucts v, ±5V, ±2. ±10V, 0~+5 ctable)	commar PE 5V; ;v	AIO- ub board Unipol 0~+1.2 selecta	16ch Multiplexer of ATCH-I6A(PCI) Can add 16 angle- differential inputs. 121601E32-PE 1 r ±10V, ±5V, ±2.5V far: 0-+10V, 0-+5V ±2V (jumper or softwable)
Expr 100kS, Analog AlO-12 Model Input chann Output chann Resolution	CI 'ESS 's 12-bit 1/O 21601E: els inels Range Gain Conessin Speed	Loch Loch	Ich Ich Inux Ich Inux Ich da Varie The sta Input de Feature Feature	Jan Ivo 4/4 Driver ata buffer ta buffer ty of accert/end of state competence ata competence	Counter 1ch C-LOG memory essories c sampling c arison or c tre-based 1601E3-P 2 single-end umper selecta (Max.)	E series GER (FIFO o can external calibra E ed, 16 diff able)	Memory on Board MATL or RINC erend furn perform TTL-lev tion fur AIC ferential ir 12bi Bipo Unip (jum	AB C buffe ictions ied by solutions ied by s	LabVIE er) software ut software ut channel m V, ±5V, ±2. +10V, 0~+5 ctable) ax.)	commar PE 5V; iv	AlO- ub board Bipola Unipol 0~+1.: selectr	16ch Multiplexer 5 ATCH-16A(PC)) Can add 16 singlexer differential nights. 121601E3-PE) r: ±10V, ±5V, ±2.5V (arc 0-+10V, 0-+5V 25V (jumper or softy able) c/ch (Max.)
Expr 100kS, Analog AlO-12 Model Input chann Output chann Resolution	CI 'CSS 'S 12-bit J 1/O 21601E3 els nels Range Gain Convesion Speed	Alo-161601UE3 16ch Windows Driver Multi-function 3-PE NEW Alo-161601UE3 16 single-ended, 8 difference 16bit Bipolar: ±10V, ±5V;Unit - 1ysec/ch (Max.)	Ich Ich Inux Inux Inux Ich Inux Inu	Joint and a second s	Counter C-LOGO remory essories c sampling arison or e tre-based 1601E3-P 2 single-endo	E series GER (FIFO of can external calibra E ed, 16 diff able)	Memory on Board MATL OOR RINC eend funn perform TTL-lev tion fur AIC ferential in 12bi Bipo Unio (jum	AB a buffe a buffe	LabVIE er) software ut channel m V, ±5V, ±2. +10V, 0-+5 ctable) ax.)	Comman PE 5V; 5V	AIO- ub board Bipola Unipol 0-+1.: selecta 10µse ±25.58	16ch Multiplexer 3 ATCH-16A(PC) Can add 16 singlex differential inputs. 121601E3-PE) r: ±10V, ±5V, ±25V (arc 0+10V, 0-45V 25V (umper or softv able) c/ch (Max.) (dl ±10V, ±5V, -0.27)
Expr 100kS, Analog AlO-12 Input chann Output chann Resolution	VS 12-bit J/O 21601E: els inels Range Casin Conesion Speed Conesion Speed	Bipolar: ±10V, ±5U;Bl 16ch	Ich Linux 16M da A varie The sta Input da Feature PE ipolar: 0-+10V	Lagran vo 4/4 Driver ata buffer ty of accert/vlend of a ty of accert/vlend of a AIO-161 oris up to 3: /, 0-+5V (jul 10µsec/ch ±5LSB *3	Counter C-LOGO memory essories of sampling arison or e tre-based 1601E3-P 2 single-endo	E Series GER (FIFO of can extor can be external calibra ed, 16 diff able)	Memory on Board MATL or RINC end fun perform TTL-lev tion fur AIC ferential in 12bi Bipo Unip (jum 1µse ±3L3	AB a buffe actions act by s vel Inp nction D-1216 aput with t blar: ±100 objar: 0	LabVIE er) software ut channel m V, ±5V, ±2. +10V, 0-+5 ctable) ax.)	Comman PE 5V; 5V	AlO- ub board Bipola Unipol 0~+1.1 selectre ±2LSB Input), 0~+2.5	16ch Multiplexer 3 ATCH-16A(PC)) Can add 16 iniget. 8 differential injust. 121601E3-PE) r: ±10V, ±5V, ±2.5V, ±2.5V far: 0-+10V, 0-+5V 25V (jumper or softv able) c/ch (Max.) ((at ±10V, ±5V, 0-+11 ±4LSB (at ±2.5V, input))
Expr 100kS, Analog AlO-12 Model Input chann Output chann Resolution	VS 12-bit J/O 21601E: els nels Range Conesion Speed Conesion Speed Conesion Speed	Display 16ch Windows Driver Multi-function 3-PE NEW AIO-161601UE3: 16 single-ended, 8 difference 16 single-ended, 8 difference 16 bit 16 bit 16 bit 19 single-ended, 8 difference 19 single-ended, 8 difference 16 bit 19 bit - 1 ysec/ch (Max.) ±5LSB ^{r3} 100 or more 10 bit or more 10 bit	Industry Ich Linux 16M da A varie The sta Input da Feature PE ipolar: 0-+10V	Alda Driver Ata buffer ty of accert/r/end of a ty of accert/r/end of a AlO-161 orts up to 3: /, 0-+5V (jul 10µsec/ch ±5LSB *3	Counter 1ch C-LOG memory essories c sampling of arison or e tre-based 1601E3-P 2 single-ende (Max.)	E GER (FIFO of can extor can be pexternal calibra ed, 16 diff able)	Memory on Board MATL or RINC end fun perform TTL-lev tion fur AIC ferential in Bipo Unip (jum 1µse ±313	AB C buffe octions ied by s vel Inp oction D-1216 nput with t blar: ±10 ¹⁰ poolar: 0~	LabVIE or) software ut solues- channel m v, ±5V, ±2. +10V, 0-+5 ctable) ax.)	Comman PE 5V: SV	AIO- ub board Bipola Unipol 0-+1.2 selects ±2LSB Input), 0-+2.5	16ch Multiplexer 3 ATCH-16A(PC) Can ad 16 inspect 3 differential inputs. 121601E3-PE) r: ±10V, ±5V, ±2.5V, tar.0 - +5V y ch(maper or softwable) c/ch(Max.) (at ±10V, ±5V, 0-+11 ±4LSB (at ±2.5V, at ±3) y, 0-+1.25V input)
Expr 100kS, Analog AlO-12 Input chann Output chan Resolution	Vs 12-bit J/O 21601E: Range Gain Convesion Speed Convesion Speed Impedance Range Rating	Display 16ch Windows Driver Multi-function 3-PE NEW AlO-161601UE33 16 single-ended, 8 diffe 16 single-ended, 8 diffe 16bit - 1µsec/ch (Max.) ±5LSB *3 1MQ or more Bipolar: ±10V; Unipola ±5mA	Ich	Upper lange Upper lange Alta Driver Atta Duriver Atta Duriver Alta Duriver Alta	Counter 1ch C-LOG memory essories c sampling of arison or e tre-based 1601E3-P 2 single-ende umper selectu (Max.) able)	GER (FIFO of can external calibra ed, 16 diff able)	Memory on Board MATL Or RINC end fun perform TTL-let tion fur AIC ferential ir 12bi Bipo Unip (jum 1µse ±3L3	AB a buffe a buffe	LabVIE or) software ut solues- channel m v, ±5V, ±2. +10V, 0-+{ ctable) ax.) v, ±5V; Un	PE polar: 0~+*	AlO- ub board Unipol 0-+1.2 selecta 10µse ±2LSB Input), 0-+2.2 selecta	16ch Multiplexer 3 ATCH-16A(PC) Canada 16 singlexer 3 add 16 singlexer 14 add 16 singlexer 14 add 16 singlexer 14 address 14 a
Expr 100kS, Analog Alo-12 Input chann Output chan Resolution Analog Input	Vis 12-bit Vis 12-bit Vio 21601E: els nets Range Gain Conesion Speet Conesion Speet Conesion Speet Conesion Speet	Display 16ch Windows Driver Multi-function 3-PE NEW AlO-161601UE3 16 single-ended, 8 diffe 16 single-ended, 8 diffe 16bit Bipolar: ±10V, ±5V:Uni - 1 ±5LSB *3 1MQ or more Bipolar: ±10V; Unipola: ±5mA 10ysec/ch (Max.)	ipolar: 0-+10V (jur	Lagrand Lagrand Driver Atta buffer tata buffer ty of accer transform AlO-161 orts up to 32 /, 0-+5V (juu 10µsec/ch ±5LSB *3 mper selecta	Counter 1ch C-LOGO memory essories c sampling of arison or e tre-based 1601E3-P 2 single-ende amper selecta (Max.) able)	GER (FIFO 0 can external calibra E e able)	Menory on Board MATL Dor RINCCore Perform TTL-left Bipc Unip (jum 12bi Bipc Unip (jum 12bi Bipc Unip (jum 12bi Bipc Unip (jum 23L) Bipc (jum 25L) Bipc (jum 25L) Bipc (jum) (j	AB 3 buffe ctions vel Inp nction D-1216 put with t t ss ss ss ss ss ss ss ss	LabVIE software ut channel m v, ±5V, ±2. ±10V, 0-+5 ctable) ax.)	polar: 0-++	hd, AlO- Uubboard Uub	16ch Multiplexer 3 ATCH-16A(PC) Can ad 16 singlexer 3 ad inferential inputs. 121601E3-PE) r: ±10V, ±5V, 225V (umper or softwable) c/ch (Max) (id±10V, ±5V, 0-+1) ±4LSB (id±25V, 3*) \$V, 0-+1, 25V input) per selectable)
Expr 100kS/ Analog Alo-12 Input channed Output channed Output channed Resolution	VS 12-bit J/O 21601E: Range Gain Conversion Speed Conversion Speed	Bit Part 16ch Windows Driver Multi-function 3-PE NEW Alo-161601UE3 16 single-ended, 8 diffe 16 single-ended, 8 diffe 16bit Bipolar: ±10V, ±5V;Uni - - 1µsec/ch (Max.) ±5LSB *3 1MQ or more Bipolar: ±10V; Unipola ±5mA 10µsec/ch (Max.) ±3LSB 10 or eless	ipolar: 0-+10V (jur	Univer 4/4 Driver tata buffer ty of accert/end of a tata compares software AIO-161 orns up to 3: /, 0-+5V (jul 10µsec/ch ±5LSB *3 mper selectare	Counter 1ch C-LOGO memory essories of sampling of sampling of arison or e tre-based 1601E3-P 2 single-ende (Max.) able)	GER (FIFO 4) (FIFO 4) call bra callbra callbra edd, 16 diff	Menory on Board MATLL AATLL-levend funn Perform TTL-levend funn 12bi Bippo Unip (jum 12bi Bippo Unip (jum 14bi Bippo (jum 14bi Bippo (jum) 14bi Bippo (jum)	AB 3 buffe ctions buffe ed by ty vel Inp nction D-1216 blar: ±10' blar: ±10' sB blar: ±10' sB blar: ±10' control (Massimum sb blar: ±10' blar:	LabVIE software ut contues- co	PE polar: 0-+*	nd, AIO- Uuipobard Uuipobard Select 10pss select 10pss select 10pss 10v (jump	16ch Multiplexer t ATCH-164(PCI) Can add 16 single- 8 differential input. 121601E3-PEE) r: ±10V, ±5V, ±2.5V far: 0-+10V, 0-45V ±25V (jumper or softv able) c/ch (Max.) c/ch (Max.) st42B8 (at 22.5V, ± 5V, 0-+1.25V input) ber selectable)
Expr 100kS, Analog AlO-1: Model Input chann Output chan Resolution Analog Input	As 12-bit Vo 12-bit Vo 21601E: els mels Range Gan Conesin Speed Conesin	Display 16ch Windows Driver Multi-function 3-PE NEW AlO-161601UE33 I6 single-ended, 8 diffe 16 single-ended, 8 diffe 1ch 19 sec/ch (Max.) ±5LSB *3 1MΩ or more Bipolar: ±10V; Unipola ±5IRA 10 yasec/ch (Max.) ±3LSB 3 1Ω or less Software command, In	I tohn Linux I A varie The sta Input de Feature PE ipolar: 0-+10V (jur put data comp	Upper lange Upper lange Alt Driver Alt Driver Ita buffer tata buffer ty of accert/end of accertification of accertification accertititaccertification accertitation accertitaccertification acc	Counter 1ch C-LOG(r memory essories c sampling arison or e re-based 1601E3-P 2 single-ende (Max.) able)	Eseries (FIFO 0) (an external can external external ed, 16 diff	Memory on Board MATL MATL Perform TTL-leie ALC Gerential in 12bi Bipc Unip (jum 12bi Bipc Unip (jum 12bi Bipc 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	AB 3 buffet ictions iced by vel Inp iction 0-1216 iction 0-1216 iction iction ictions icti	LabVIE software ut channel m v, ±5V, ±2. +10V, 0-+5 ctable) ax.) v, ±5V; Un ax.)	Commar PE 5V: SV:	hd, AIO- Uuipobard	If Sch Multiplexer f Arch-fa/CQU Can add 16 angle- adferential inputs. 121601E3-PEE r ± 10V, ±5V, ±2.5V far co-tal low, 0-45V 25V (jumper or softwable) c/ch (Max.) (at ± 25V, ±4LS (u ± 25V, ±4LS (u ± 25V, ±5V, 0-+1.25V input) per selectable)
Expr 100kS, Analog AlO-12 Model Input channo Output chan Resolution Analog Input	As 12-bit (VO 21601E: Ange Gain Conesin Speed Conesin Cone Conesin Cone Conesin Cone Cone Cone Cone Cone Cone Cone Cone	Display 16ch Windows Driver Multi-function 3-PE NEW AlO-161601UE33 Ibit 16 single-ended, 8 diffe Ich 16bit Ibit - Ipsec/ch (Max.) ±SLSB *3 1MΩ or more Bipolar: ±10V; Unipola ±5mA 10psec/ch (Max.) ±3LSB 1Ω or less Software command, In	r: 0-+10V (jur	Updata Al/4 Driver Al/4 Driver tata buffer ty of accert/end of atta compares softward AIO-161 oris up to 32 /, 0-+5V (jul 10µsec/ch ±5LSB *3 mper selecta parison or E	Counter Ich C-LOG(memory essories c sampling arison or e tre-based 1601E3-P 2 single-ende (Max.) able) xternal TTL-1	Eseries (FIFO 0) can extra can be can be cal be can be cal ba cal be cal be ca	Menory on Board MATL MATL Perform TTL-leiend ALC Vinip (um 12bi Bipo Unip (um 12bi Bipo Unip (um 12bi Bipo Unip (um 12bi Bipo Unip (um 12bi 12bi 12bi 12bi 12bi 12bi 12bi 12bi	AB S buffet ictions iced by vel Inp iction D-1216 diar: ±101 diar: ±10	LabVIE er) software ut channel m V, ±5V, ±22. +10V, 0-+5 ctable) ax.) V, ±5V; Un ax.)	Commar PE 5V; SV	AlO- Bipola Bipola 0-+1.1 Select: 10pse ±2LSB Input) 0-+2.5 100V (jump)	16ch Multiplexer t ATCH-16A(PCI) Can add 16 angle afferential negation 121601E3-PEE r: ±10V, ±5V, ±2.5V 121601E3-PEE r: ±10V, ±5V, ±2.5V fair: 0:+10V, 0:+5V 25V (jumper or softv able) c/ch (Max) (id:±10V, ±5V, 0:+1.25V input) ber selectable)
Expr 100kS, Analog AlO-12 Model Input channo Output chann Resolution Analog Input Analog Input	As 12-bit VS 12-bit VO 21601E: els nels Range Conesin Speed Conesin Conesin Speed Conesin Speed Conesin Speed Conesin Conesin Speed Conesin Conesin Speed Conesin Conesin Speed Conesin Conesin Conesin Cone Conesin Cone Conesin Cone Conesin Cone Cone Conesin Cone Cone Cone Cone Cone Cone Cone Cone	Display 16ch Windows Driver Multi-function Audit-function Second	Ich	Updata Al/4 Driver Atta buffer Atta competence Atta compe	Counter Ich C-LOG(memory essories c sampling arison or c re-based IGO1E3-P 2 single-ende (Max.) able) xternal TTL-	series GER (FIFO (can bet can bet can bet can bet calibra calibra calibra d, 16 diff able)	Memory on Board MATL Dor RINCC or RINCC Perform TTL-lee Alton Unip (jum 12bi Bippo Unip (jum 12bi Bippo Unip (jum 12bi Bippo (jum 43L 43L 44 40n-isola	AB Constructions and by several topological continuous and the several topological continuous and	LabVIE er) software ut channel m V, ±5V, ±2, table) ax.) V, ±5V; Un ax.)	Comman PE 5V; 5V; 5V; 5V; 5V; 5V;	Alo- Alo- Unipoto Select: 10µse ±2LSB Input). 0+2.5 0+	16ch Multiplexer 15 ATCH-16A(PC)) Can add 15 singlex eiffernial night. 121601E3-PE) r: ±10V, ±5V, ±2.5V (arc 0-+10V, 0-+5V 25V (jumper or softy able) c/ch (Max) (af±10V, ±5V, 0-+11 ±4LSB (af±2.5V, ±2 SV, 0-+1.25V input) per selectable) unput is jumper selecta
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Exptr 100kS, Analog Ano-1: ^{Model} Input chann Output chann Output chann Output chann Output chann Resolution Analog Input Analog Output Trigger Isolation typ Timer Digital I/O Interrupts I/O Address Power Consum Bus / Dimen Connector Obtions	Vs 12-bit VS 12-bit VO 21601E: Range Gain Conesin Speed Conesin Speed Conesin Speed Conesin Speed Conesin Speed Conesin Conesin Conesin Cone Conesin Cone Cone Cone Conesin Cone Cone Cone Cone Cone Cone Cone Cone	Display 16ch Windows Driver Multi-function 3-PE NEW 16 single-ended, 8 diff 1ch 16 single-ended, 8 diff 1ch 16 single-ended, 8 diff 1ch 16 bit 1ch 19bit 1ch 19bit 1ch 19bit 1ch 19bit 1ch 19bit 1ch 19bit 1ch 10bit 1ch 10ch 1ch	It is in the second sec	Logital Logital Al/4 Driver Al/4 Driver tata buffer ty of accert/end of a tata compares software AIO-161 orts up to 32 /, 0-+5V (jul) 10µsec/ch ±5LSB *3 nper selectare parison or E e or counter in +3.3V 1500 :1.0a ×1 / 11 -40UNC, DO SEN-DAP1 / 12* SEN-DAP1 / 12*	Counter Ich C-LOG(memory essories c sampling arison or e re-based 1601E3-P 2 single-ende (Max.) able) xternal TTL-1 able) xternal TTL-1 (Max.)	GER (FIFO d (FIFO d can external exxternal exxternal d d, 16 diff able)	Memory on Board MATL bor RINCCor Part Intro- tion fur Corrential in 12bi Bipco Unip (jum 12bi Bipco Unip (jum 12bi Bipco Unip (jum 12bi Bipco Unip (jum 12bi 12bi Bipco Unip (jum 12bi 12bi 12bi 12bi 12bi 12bi 12bi 12bi	AB AB AB AD AD AD AD AD AD AD AD AD AD	LabVIE er) software ut channel m v, ±5V; ±2 t10V, 0-4 ctable) ax.) v, ±5V; Un ax.) v, ±5V; Un ax.)	Comman PE SV: SV: polar: 0~+1 ommon use o GA(PC):*11	Allo Allo Md, Allo Mub board Bipola Mub boar	16ch Multiplexer of TCH-154(PCI) Can add 16 single- s differential inputs. 121601E3-PE) r: ±10V, ±5V, ±2.5V (ar: 0-±10V, 0-±5V 25V (jumper or softy able) cr(h (Max.)) cr(at ±2.5V, ±2.5V (ar: 0-±10V, 0-±5V 25V (jumper or softy able) cr(h (Max.)) cr(at ±2.5V, ±2.5V input) per selectable) aunput is jumper selecta 1500mA
Exptr 100kS, Analog AlO-12 Model Input channo Output chann Resolution Analog Input Analog Input Analog Output Trigger Isolation typ Timer Digital I/O Interrupts I/O Address Power Consum Bus / Dimen Connector Options	Vs 12-bit VS 12-bit VO 21601E: Range Gain Conesion Speed Conesion	Displit 16ch Windows Driver Multi-function 3-PE NEW 16 single-ended, 8 diffe 1ch 19 sec/ch (Max.) ±5LSB *3 1MQ or more Bipolar: ±10V; Unipola ±5LSB *3 10 ysec/ch (Max.) ±3LSB 10 or less Software command, In - - + 4 Non-isolad TTL-level in 10 or less Software command, In - + +3.3V 2000mA PCI Express Base Spe 37-pin female D-types 10 preas* 37-pin female D-types 10 preas* 10 preas* 10 preas* 10 preas* 10 preas* 10	r: 0-+10V (jur put data comp put (Common us control rest), PC-16	by the format of the second s	Counter Ich C-LOG(memory essories c sampling arison or e tre-based I601E3-P 2 single-ende (Max.) able) xternal TTL-1 (Max.) able) OmA 69.33(L)×111 CLC-J37E Jor 1C [J37E] Article 1C [J	evel Inpu electable)	Memory on Board MATL bor RINCC market MATL Correction fur All Bipo Unip (jurn 1) siste sis	AB AB AB AB AB AB AB AB AB AB	LabVIE pr) software ut solute3- channel m v, ±5V; ±2. +10V, 0-+5 ctable) ax.) v, ±5V; Un ax.) vel output (C nA t sA*5, ATCH- S-1.5/3, PC	Comman PE SV; SV; polar: 0-++ ommon use o isA(PC))+11, /A	Alo- Alo- Unipolitical Alo- Unipolitical Alo- Unipolitical Alo- Unipolitical Alo- Unipolitical Alo- Unipolitical Alo- Unipolitical Alo-	16ch Multiplexer f ATCH-16A(PC) Can add 16 angles adferential regulation 121601E32-PE r: ±10V, ±5V, ±2.5V 121601E32-PE r: ±10V, ±5V, ±2.5V 25V (jumper or softvable) crick (Max.) cridt (add 2.5V, ±2.5V) sable) crick (Max.) crick (Max.) crick (Max.) sable) crick (Max.) crick (Max.) sable)
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Expr 100kS, Analog Ano-12 Input channo Output chann Output chann Resolution Analog Input Analog Input Analog Output Trigger Isolation typ Timer Digital I/O Interrupts I/O Address Power Consum Bus / Dimen Connector Options Notes	Voi 12-bit Voi 12-bit Voi 12-bit 21601E: Range Conesin Speed Conesin Spe	Display 16ch Windows Driver Multi-function 3-PE NEW AlO-161601UE33 16 single-ended, 8 diff 16 single-ended, 8 diff 1ch 16bit 16bit Bipolar: ±10V, ±5V;Uni 1 - 1µsec/ch (Max.) ±5LSB *3 1MΩ or more Bipolar: ±10V; Unipola 5mA 10µsec/ch (Max.) 3LSB ±3LSB 10µsec/ch (Max.) ±3LSB 20 or less Software command, In - - 4 Non-isolated TL-level in 1 Gorpies 32 ports +3.3V 2000mA PCI Express Base Spa 57-pin female D-type S 16-pin Pin Header con 7-pin Finale D-type S 10-pin SP (SP (SP (SP (SP (SP (SP (SP (SP (SP	It in the second secon	Driver 4/4 Driver Atta bufferty	Counter Ich C-LOG(remory essories C sampling (arison or c tre-based IGO1E3-P 2 single-ende IGO1E3-P 2 single-ende (Max.) able) xternal TTL-1 (Max.) able) xternal TTL-1 CLC-J37SAF IC (JAE] or . EPD-374 ⁴⁵⁷ , Sa7950.5P ¹ C, 40.15 IS f operational amount of the same of t	esries (FIFO ((FIFO (an external externa	Memory on Board MATL Dor RINCCor Perform TTL-lee Alton Unip Unip Unip Unip Unip Unip Unip Uni	AB AB AB AD AD AD AD AD AD AD AD AD AD	LabVIE pr) software ut channel m V, ±5V, ±2. t10V, 0-45 ctable) ax.) V, ±5V; Un ax.) vel output (C ax.) evel output (C ax.) input was se input was se	Comman PE SV; SV; polar: 0-+* ommon use of GA(PC))*1, / GA(PC)*1, / GA(PC)*1, / COM2 comman COM2 co	Alo- Alo- Image: Alor of the second se	16ch Multiplexer 1 ATCH-16A(PC)) Can add 16 singlexer differential number 121601E3-PE) r: ±10V, ±5V, ±2.5V (arc 0-+10V, 0-+5V 25V (jumper or softy able) c/ch (Max.) (at ±10V, ±5V, 0-+11 ±4LSB (at ±2.5V, ±2V) (at ±
Expr 100kS, Analog Ano-12 Input channo Output channo Output channo Output channo Analog luput Analog luput Trigger Isolation typ Timer Isolation typ Timer Solation typ Timer Solation typ Timer Connector Options Notes	Vs 12-bit VS 12-bit VS 12-bit 1/O 21601E: Range Gain Conesion Speed Conesion Speed Co	Display 16ch Windows Driver Multi-function 3-PE NEW AlO-161601UE33 Signale-ended, 8 diff 16 single-ended, 8 diff Signale-ended, 8 diff 16bit Signale-ended, 8 diff 19belar: ±10V, ±5V:Uni Signale-ended, 8 diff ±5LSB *3 Signale-ended, 8 diff 10ysec/ch (Max.) ±5LSB *3 10x0 or more Bipolar: ±10V; Unipola ±5mA 10ysec/ch (Max.) ±3LSB Software command, In 10 or less Software command, In 20 or less Software command, In 3.3V 2000mA PCIExpress Base Spe 7-pin female D-type S Singersprint 6.4x00mA PCIExpress Base Spe 7-pin Female D-type S Singersprint 6.4x00mA PCIExpress Base Spe 7-pin female D-type S <	It in the set of the	Dytar 4/4 Alta Driver atta buffer ty of accc xtra buffer y of accc AIO-161 orts up to 3: AIO-161 orts up to 3: /, 0-+5V (ju 10µsec/ch ±5LSB *3 mper selecta parison or E e or counter in +3.3V 1500 :1.0a x1 / 11. +40UNC, DO SEN-D4P1-1 =5/P/15P, F0*5* >5/P/15P, F0*0 n is is recommerse secrew does se screw does	Counter Ich C-LOG(memory essories c sampling (arison or c re-based 1601E3-P 2 single-ende 1601E3-P 2 single-ende (Max.) able) xternal TTL-1 (Max.) able) xternal TTL-1 (Max.) able) (Max.) (Max	evel Inpu electable)	Memory on Board MATL Variable Constraints MATL All Constraints Martice	AB AB AB AB AB AB AB AB AB AB	LabVIE pr) software ut software channel m v, ±5V, ±2. table) ax.) v, ±5V; Un ax.) veloutput (C ax.) eveloutput (C input was se in the CN1 c s DT-E3 and is required.	Comman PE SV; SV; polar: 0-+1 ommon use of 6A(PC))*1, / A15P*0, PC ected. coll spin spin spin spin spin spin spin spin	Alo- Alo- Unipolo Unipolo State 10us 22LSB Input, 22LSB Input, 22LSB 10us 22LSB 10us 22LSB 10us 22LSB 10us 22LSB 10us 22LSB 10us 22LSB 200V (jump 210V (jump	16ch Multiplexer 4 ATCH-16A(PC) Can add 16 singlex differential inputs. 121601E3-PE) r: ±10V, ±5V, ±2.5V lar: 0-+10V, 0-+5V 25V (umper or softy able) c/ch (Max.) :(at ±10V, ±5V, 0-+11 ±4LSB (at ±2.5V, ±2) vol.0-+1.25V input) ver selectable) unput is jumper selecta 1500mA PC)) ⁺² , DICT-375 ⁴ , D , DT/E1, DT/E2, CN5-

					Low-cost Mult	i-function L series	Analog	I/O
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		 1K data Functio AD16-6 Low Press 	i buffer memory ns, Connector p i4(LPCI)LA ofile PCI - compl	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatib bracket for use in stand	ole with the PCI-co dard PCI slot)	mpliant board	Digital I/
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Model	Channels	● 1K data ● Functio AD16-6 ● Low Pro AIO-160802LI-PE 8 single-ended	A buffer memory ns, Connector p 44(LPCI)LA ofile PCI - compl Al-1616LI-PE 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatib bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti	ale with the PCI-co	mpliant board	Digital I/O Com
Model	Channels Range Impedance		Luffer memory ns, Connector p 44(LPCI)LA ofile PCI - compl Al-1616LI-PE 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatib bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different	ale with the PCI-co	mpliant board	Digital I/O Communic
Model Analog Input	Channels Range Impedance Resolution		Luffer memory ns, Connector p 4(LPCI)LA ofile PCI - compl Al-1616LI-PE 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatib bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti	ale with the PCI-co	mpliant board	Digital I/O Communication
Model Analog Input	Channels Range Impedance Resolution Conversion Speed Conversion Accuracy ***		t buffer memory ns, Connector p 4(LPCI)LA ofile PCI - compl AI-1616LI-PE 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatib bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB	al with the PCI-co	mpliant board	Digital I/O Communication Mo
Model Analog Input	Channels Range Impedance Resolution Conversion Speed Conversion Acuacy ** Buffer Memory Channels		Louffer memory ns, Connector p 4(LPCI)LA ofile PCI - compl AI-1616LI-PE 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatit bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different ±5LSB	al with the PCI-co	mpliant board	Digital I/O Communication Motion C
Model Analog Input	Channels Range Impedance Resolution Conversion Speed Consection Association Buffer Memory Channels Range		Luffer memory ns, Connector p 4(LPCI)LA ofile PCI - compl AI-1616LI-PE 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatit bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different ±5LSB	al with the PCI-co	mpliant board	Digital I/O Communication Motion Control
Model Analog Input	Channels Range Impedance Resolution Convesion Speet Convesion Speet Channels Range Impedance Resolution		Luffer memory ns, Connector p (4(LPCI)LA ofile PCI - compl Al-1616LI-PE 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatib bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB	al e with the PCI-co	mpliant board	Digital I/O Communication Motion Controller
Model Analog Input Analog Output	Channels Range Impedance Resolution Conesion Assay ¹⁷⁴ Buffer Memory Channels Range Impedance Resolution Conversion Speed		Luffer memory ns, Connector p (4(PCI)LA ofile PCI - compl 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatit bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different ±5LSB	al	mpliant board	Digital I/O Communication Motion Controller Bus
Model Analog Input Analog Output	Channels Range Impedance Resolution Conversion Speed Conversion Speed Conversion Speed Conversion Speed Conversion Speed Conversion Speed Conversion Speed	AlO-160802LI-PE Single-ended Bipolar: =10V 10ysec/ch (Max.) ±16LSB 1K word 2ch Bipolar: =10V 10y ese (Max.) ±5LSB 1K word	Louffer memory ns, Connector p (4(LPCI)LA ofile PCI - compl 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatit bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different ±5LSB	al	mpliant board	Digital I/O Communication Motion Caritoller Bus Adat
Model Analog Input Analog Output Digital I/O	Channels Range Impedance Resolution Conesio Rped Conesio Ruang ¹⁷⁵ Buffer Memory Channels Range Impedance Resolution Conesion Sped Conesion Assag Buffer Memory Input Output	AltO-160802LI-PE Single-ended Bipolar: ±10V 104 correct Aido-160802LI-PE Single-ended Bipolar: ±10V 104 correct Aido-160802LI-PE Single-ended Bipolar: ±10V 104 Correct Aido-1608 A	buffer memory ns, Connector p v4(LPCI)LA ofile PCI - compl AI-1616LI-PE 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatit bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different ±5LSB	al	mpliant board	Digital I/O Communication Motion Carticoler Bus Adapter
Model Analog Input Analog Output Digital I/O	Channels Range Impedance Resolution Conesin Assay ⁷⁰ Buffer Memory Channels Buffer Memory Input Conesin Assay Buffer Memory Input Channels Counting	AltO-160802LI-PE Single-ended Bipolar: ±10V 104 00 more 16bit 10ysec(h(Max.) ±16LSB 1K word 2ch Bipolar: ±10V 100 ress 16bit 10ysec(Max.) ±5LSB 1K word 4 Non-isolated TTL-level input (pc 4 Non-isolated TTL-level output (pc 1ch	Louffer memory ns, Connector p (4(LPCI)LA ofile PCI - compl 16 single-ended 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatit bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different ±5LSB	al	mpliant board	Digital I/O Communication Motion Cartholer Bus Adapter Us
Model Analog Input Analog Output Digital I/O Counter	Channels Range Impedance Resolution Conversion Speed Consense Nazary Buffer Memory Channels Resolution Conversion Speed Conversion Assared Buffer Memory Input Channels Counting Max. count	AlO-160802LI-PE Single-ended Bipolar: 10V 104 or more 16bit 1049800 104980 10498 10498 104 104980 104	Louffer memory ns, Connector p (4(LPCI)LA ofile PCI - compl 16 single-ended 16 single-ended	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatit bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different ±5LSB	al	mpliant board	Digital I/O Communication Motion Cartholier Bus Adapter User Sc
Model Analog Input Analog Output Digital I/O Counter Interrupts I/O Address/	Channels Range Impedance Resolution Conversion Speed Comercia Nature Impedance Resolution Conversion Assare Buffer Memory Input Output Channels Counting Max. count		Louffer memory ns, Connector p (4(PCI)LA ofile PCI - compl 16 single-ended - - - - - - - - - - - - - - - - - -	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatit bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different ±5LSB	al	mpliant board	Digital I/O Communication Motion Cartholier Bus Adapter User Service
Model Analog Input Analog Output Digital I/O Counter Interrupts Power Consu	Channels Range Impedance Resolution Conversion Speed Comersion Resolution Channels Range Impedance Resolution Conversion Resolution Conversion Resolution Conversion		a buffer memory ns, Connector p (4(PCI)LA ofile PCI - compl 16 single-ended 16 single-ended - - - - - - - - - - - - - - - - - -	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatit bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 different ±5LSB	al	mpliant board	Digital I/O Communication Motion Cartroller Bus Adapter User Service
Model Analog Input Analog Output Digital I/O Counter Interrupts Power Consu Bus / Dimen: Conso tio:	Channels Range Impedance Resolution Conversion Speed Consens Nazary Buffer Memory Channels Resolution Conversion Assary Input Output Channels Counting Max. count max. count mption (Max.) sions (mm)		Al-1616L1-PE Al-1616L1-PE 16 single-ended - - - - - - - - - - - - -	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatib bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB 3.3VDC 620mA R0Epes Bas Spalate Re:16 #1/12.68 68-pin 0.8mm Pitch connector	al	mpliant board	Digital I/O Communication Moleon Controller Bus Adapter User Service & & C
Model Analog Input Analog Output Digital I/O Counter Interrupts Power Consu Bus / Dimen Connector	Channels Range Impedance Resolution Conversion Speed Consense Nazary Buffer Memory Channels Resolution Conversion Assary Input Output Channels Counting Max. count mption (Max.) sions (mm)		Al-1616LI-PE Al-1616LI-PE 16 single-ended - - - - - - - - - - - - -	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatib bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB 3.3VDC 620mA R0Epes Bas Spolator Re:16 x1 / 12,68 68-pin 0.3mm Pitch connector HDR-658WTLFDT-SL (HONDA) or ex	Lift 20(4)	mpliant board	Digital I/O Communication Moleon Controller Bus Adapter User Service & Cable
Model Analog Input Analog Output Digital I/O Counter Interrupts Power Consu Bus / Dimen: Connector	Channels Range Impedance Resolution Conversion Speed Comersion Resolution Channels Range Impedance Resolution Conversion Resolution Conversion Resolution Conversion		Al-1616LL-PE Al-1616LL-PE 16 single-ended - - - - - - - - - - - - -	(FIFO or RIN n and Signal iant (includes	G buffer) assignment is compatib bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB 3.3VDC 620mA R0Epes Bas Spolation Re:16 x1 / 12166 Be-pin 0.9 Spolation Re:	ble with the PCI-co dard PCI slot)	mpliant board	Digital I/O Communication Molion Controller Bus Adapter User Service & Cables
Model Analog Input Analog Output Digital I/O Counter Interrupts Power Consu Bus / Dimen: Connector Options	Channels Range Impedance Resolution Conversion Speed Comesion Rayary Buffer Memory Channels Resolution Conversion Speed Conversion Assace Buffer Memory Input Output Channels Counting Max. count mption (Max.) sions (mm) Software Accessories	AlO-1608002LI-PE Single-ended Bipolar: ±10V Indu or more 16bit 10µsec/h (Max.) ±16LSB 1K word 2ch Bipolar: ±10V 10 or less 16bit 10µsec/h (Max.) ±16LSB 1K word 2ch Bipolar: ±10V 10 or less 16bit 10µsec (Max.) ±5LSB 1K word 4 Non-isolated TTL-level input (pc 2cht (binary data) 1 level Occupies 64 ports 3.3VDC 820mA PCI Express Base Specification F 10250-52A2JL [3M] or equivalent ACX-PAC(Wa2) EP-50A ^{+ad} , ATBA-BL ^{+3-4d+6} , AT ATP-8L ⁺³⁻⁴ , DICT-50S ⁺³ , DICT-50	Al-1616LL-PE Al-1616LL-PE 16 single-ended - - - - - - - - - - - - -	(FIFO or RIN n and Signal iant (includes)×110.18(H)	G buffer) assignment is compatib bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB 3.3VDC 620mA R0Epes Bas Spondato Re:16 x1 / 12.66 Be-pin 0.80 Stand Picto Action DRA-668WILEDT-SL (HONDA) or a DTP-44PC/**1, PD-68.4***11, ATP-2 DFCA68P5 LOCASP5 D-01	ble with the PCI-co dard PCI slot)	mpliant board	Digital I/O Communication Molion Controller Bus Adapter User Service & Cables Fla
Model Analog Input Analog Output Digital I/O Counter Internuts I/O Address Power Consu Bus / Dimen: Connector Options	Channels Range Impedance Resolution Conversion Speed Comession Rayary Buffer Memory Channels Resolution Conversion Speed Conversion Resolution Conversion Resolution Conversion Resolution Conversion Resolution Max. count Max. count Inputon (Max.) sions (mm) Software Accessories Cables / Correctos		Label Content of the second of	(FIFO or RIN n and Signal iant (includes)×110.18(H)	G buffer) assignment is compatible bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB 3.3VDC 620mA PC Epus Bas Spondato Re:16 x1 / 12.68 Be-pin 0.900 Min Pitch consect HDR-468WILEDT-SL (HONDA) or a DTP-44PC/**1, PD-684***1, ATP- CPCA8EPS-0.5P/1.5P, ADC-88 PC08EPS-0.5P/1.5P, ADC-88	al a	mpliant board	Digital I/O Communication Molion Controller Bus Adapter User Service & Cables FlavNeti
Model Analog Input Analog Output Digital I/O Counter Interrupts I/O Address Power Consu Bus / Dimen: Connector Options Notes	Channels Range Impedance Resolution Conversion Speed Comesion Rayary Buffer Memory Channels Resolution Conversion Speed Conversion Assace Buffer Memory Input Output Channels Counting Max. count mption (Max.) sions (mm) Software Accessories Cables / Correctos	AlO-1608002LI-PE Single-ended Bipolar: ±10V IMQ or more Ibbit 10µsec/h (Max.) ±16LSB IK word Zeh Bipolar: ±10V 10 or less Ibbit 10µsec/h (Max.) ±16LSB IK word Zeh Joysec (Max.) ±5LSB IK word AlO-1608001 Ibbit 10µsec (Max.) ±5LSB IK word Alon-isolated TTL-level input (pc 4 Non-isolated TTL-level input (pc 2-bit (binary data) 1 level Occupies 64 ports 3:3VDC 820mA PCI Express Base Specification F 10250-52A2JL [3M] or equivalent ACX-PACW20 EPD-50A ⁺³⁻⁴ , ATBA-8L ⁺³⁺⁴⁺⁶⁴ , AT ATP-8L ⁻³⁻⁷ , DICT-50S ⁻⁷ , DIC	Land Content of the second of	(FIFO or RIN n and Signal iant (includes)×110.18(H))×110.18(H) 5P LSB non-linearity er al amplifier *2	G buffer) assignment is compatible bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB 3.3VDC 620mA ROEpes BasSedato Re:18 x1/12.65 68-pin 0.9km Pitch compatible DRAc68WILEDT-SL (HONDA) or el DDRAC68WILEDT-SL (HONDA) or el DDRAC68WILEDT-SL (HONDA) or el CROBERS-0.5P/1.5P, ADC-68 ror may occur.	al a	mpliant board	Digital I/O Communication Motion Controller Bus Adapter User Service & Cables FlaxNetViewe
Model Analog Input Analog Output Digital I/O Counter Internuts I/O Address Power Consul Bus / Dimen: Connector Options Notes	Channels Range Impedance Resolution Conversion Speed Comessio Nasary Buffer Memory Channels Resolution Conversion Speed Conversion Assary Input Output Channels Counting Max. count mption (Max.) sions (mm) Software Accessories Cables / Correctos		Louffer memory ns, Connector p (4(PCI)LA ofile PCI - compl 16 single-ended 16 single-ended 1 16 single	(FIFO or RIN n and Signal iant (includes)×110.18(H))×110.18(H) 5P LSB non-linearity er al amplifier *4: *6: *6: *6: *6: *6: *6: *6: *6: *6: *6	G buffer) assignment is compatible s bracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB 3.3VDC 620mA R0Epes Bas Sediator As:18 x1/21.65 68-pin 0.900 Minute Pick And Astronomic	Jerzohn al Jerzohn gaven jerzo	mpliant board	Digital I/O Communication Motion Controller Bus Adapter User Service & Cables FlaxNetViewer
Model Analog Input Analog Output Digital I/O Counter Interrupts Power Consu Bus / Dimen: Connector Options Notes	Channels Range Impedance Resolution Conversion Speed Comesion Rayary Buffer Memory Channels Resolution Conversion Royad Conversion Royad Conversion Royad Conversion Royad Conversion Royad Differ Memory Input Output Channels Counting Max. count mption (Max.) sions (mm) Software Accessories Cables / Correctos		Land Content of the second of	(FIFO or RIN n and Signal iant (includes)×110.18(H))×110.18(H) ;P LSB non-linearity en al amplifier *4: *6: nets available *12 cables and 2 access	G buffer) assignment is compatible stracket for use in stand Al-1664LA-LPE 64 single-ended, 32 differenti ±5LSB 3.3VDC 620mA RC Deps Bas Sectors Re 16 x1 1216 69-pin 0.3 mm Pich company 69-pin 0.3 mm Pich company PD-468WILEDT-SL (HONDA) or en DTP-469PS-10.2 PD-468PS-10.2 PD PD-869PS-0.5 PI 1.5 P, ADC-68 for may occur. Only for AD-168062L-LPE, AI-1616L Maximum 0.6 analog input channels The screw-up tenrine block is used. 0. Requires optional cable PC688PS- 0. Requires optional cable PC688PS-	ble with the PCI-co dard PCI slot)	npliant board	Digital I/O Communization Motion Controller Bus Adapter User Service & Cables FlexNet/Newer Soft

Industrial Automation Products

Measurement and Control Products

Counter & Motion Controller