8316.IIb VI Function Descriptions

The functionality of Vis in 8316.llb can be classified to the following capabilities:

1. Card Configuration:

- Setup hardware base I/O address
- Setup AD/DA configuration

2. Analog Input:

- Perform one-shot single-channel analog input operation
- Perform continuous single/multiple-channel analog input operation

3. Analog Output:

- Performs single-channel analog output operation
- 4. Digital I/O:
 - Input/output digital signals
- 5. Timer/Counter:
 - Timer/counter functions

In addition, several sample programs are also included. Thorough understanding of these sample programs will help you understand how to use the library more quickly. And you are welcome to modify the sample programs for your application needs.

8316 Initial

An ACL-8316/12 card is configured according to the base address. Every ACL-8316/12 multi-function data acquisition card has to be configured by this VI before using other VI.



U8	Card_ID :	the card to be initialized, up to three cards can be initialized, the <i>Card_ID</i> must be 0,
U16	Base_Address :	1, or 2. the I/O port base address of the card, the default address on the hardware setting is
I16	Error Out :	Hex 220. Error code. <i>Error Out</i> describes the error status of this VI.

8316 Switch Card

This VI is used more than one ACL-8316/12 cards on your system. After initialized more than one ACL-8316/12 card, this VI is used to select which card is activate currently.



8316 AD Acquire

This VI is used to poll the A/D conversion data from a channel. It is an immediate, non-buffered operation, No buffering or timing is required, so the analog input data can be acquired when you need one data point from one selected channel. It is very useful for monitoring a system periodically. For ACL-8316 card, the return reading value is between -32768 and 32767. For ACL-8312, the return reading value is between 0 and 4095.



Error In : The error condition occurred before this VI I16 executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, Error Codes, for a code descriptions. the 16-bit or 12-bit A/D converted value AD Data : I16 Error code. If the Error In indicated an Error Out : I16 error, the Error Out contains the same

8316 AD DMA Cont Read

This VI performs continuous A/D conversions on the specified analog input channel at a rate as close to the rate you specified. It will transfer the data to memory (buffer) by DMA transfer.

error status of this VI.

value. Otherwise. Error Out describes the



U8	Trig Source :	 A/D trigger source 10: Internal timer pacer trigger 12: External trigger
I8	Autoscan :	0: Auto-scan is disabled1: Auto-scan is enabled
U8	Channel :	If the auto-scan is enabled, the A/D channel selection sequence is: 0,1,2,3, [<i>Channel</i>], 0,1,2,3, [<i>Channel</i>] If the auto-scan is disabled, only the [<i>Channel</i>] will be converted.
U8	AD Range :	analog input range. The possible values are: 8: Bipolar -10V to +10V 0: Bipolar -5V to +5V 1: Bipolar -2.5V to +2.5V 2: Bipolar -1.25V to +1.25V
U8	IRQ Level :	IRQ channel number, used to stop DMA. Please make sure this value is the same as the setting on your data acquisition card
U8	DMA Channel :	DMA channel number. The valid value is 5, 6, or 7.
I32	Count :	the number of A/D conversions to perform

Il6	AD Buffer :	An integer array to store the acquired data. The length of <i>buffer</i> must be equal to or greater than the value of input terminal <i>Count</i> . If auto-scan is enabled, the acquired data is stored in interleaved sequence. For example, if the value of <i>Channel</i> is 2, the data read from channel 0 is stored in <i>buffer</i> [0], <i>buffer</i> [3], <i>buffer</i> [6], the data from channel 1 is stored in <i>buffer</i> [1], <i>buffer</i> [4], <i>buffer</i> [7], the data from channel 2 is stored in <i>buffer</i> [2], <i>buffer</i> [5], <i>buffer</i> [8],
U16	c1 :	frequency divider of timer #1. If A/D trigger mode is internal pacer trigger, the sampling rate is 2MHz/(c1*c2).
U16	c2 :	frequency divider of timer #2. If A/D trigger mode is internal pacer trigger, the sampling rate is 2MHz/(c1*c2).
I16	Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error Codes</i> , for a code descriptions.
I16	AD Buffer :	An integer array containing the acquired data.
II6	Error Out :	Error code. If the <i>Error In</i> indicated an error, the <i>Error Out</i> contains the same value. Otherwise, <i>Error Out</i> describes the error status of this VI.

8316 AD INT Cont Read

This VI performs continuous A/D conversions on the specified analog input channel at a rate as close to the rate you specified. It will transfer the data to memory (buffer) by interrupt transfer.



I32	Count :	the number of A/D conversions to perform
Il6	AD Buffer :	An integer array to store the acquired data. The length of <i>buffer</i> must be equal to or greater than the value of input terminal <i>Count</i> . If auto-scan is enabled, the acquired data is stored in interleaved sequence. For example, if the value of <i>Channel</i> is 2, the data read from channel 0 is stored in <i>buffer</i> [0], <i>buffer</i> [3], <i>buffer</i> [6], the data from channel 1 is stored in <i>buffer</i> [1], <i>buffer</i> [4], <i>buffer</i> [7], the data from channel 2 is stored in <i>buffer</i> [2], <i>buffer</i> [5], <i>buffer</i> [8],
U16	c1 :	frequency divider of timer #1. If A/D trigger mode is internal pacer trigger, the sampling rate is 2MHz/(c1*c2).
U16	c2 :	frequency divider of timer #2. If A/D trigger mode is internal pacer trigger, the sampling rate is 2MHz/(c1*c2).
I16	Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error Codes</i> , for a code descriptions.
I 16	AD Buffer :	An integer array containing the acquired data.
I16	Error Out :	Error code. If the <i>Error In</i> indicated an error, the <i>Error Out</i> contains the same value. Otherwise, <i>Error Out</i> describes the error status of this VI.

8316 AD Set Autoscan

This VI is used to set automatic hardware channel scan to be enabled or disable. If the auto-scan is set as enable mode, the A/D channels can be converted automatically, that is, the hardware multiplexer will automatically increment until the channel set by 8316 AD Set Channel. And then, it will go back to channel 0. For example, the channel is set as 4, the A/D conversion sequence will be 0, 1, 2, 3, 4, 0, 1, 2, 3, 4, 0, ... If the auto-scan is set as disable, the channel will be kept as single channel only, such as channel 4.



8316 AD Set Channel

This VI is used to set A/D channel by means of writing data to the multiplexer scan channel register. There are 16 single-ended or 8 differential A/D channels in ACL-8316/12, so the channel number should be set either 0~15 or 0~7. The initial state is channel 0 which is a default setting by the ACL-8316/12 hardware configuration.



8316 AD Set FIFO

This VI is used to enable the FIFO on the ACL-8312/16. As the FIFO is enabled, all A/D converted data are stored into the FIFO. The size of A/D FIFO is 1K words on-board.



U8	Enable :	1: FIFO enable 0: FIFO disable
II6	Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error Codes</i> , for a code descriptions.
I16	Error Out :	Error code. <i>Error Out</i> describes the error status of this VI.

8316 AD Set Mode

This VI is used to set the A/D trigger source, A/D channel selection and A/D data transfer mode by means of writing data to the *AD Mode Control Register* (refer to section 4.7 in ACL-8316/12 manual). The hardware initial state of the ACL-8316/12 is set as internal software trigger with program polling data transfer.



8316 AD Set Range

This VI is used to set the A/D analog input range by means of writing data to the A/D range control register. For ACL-8316/12 card, the gain values are only support 1, 2, 4, 8 four levels. The relationship between analog input voltage range, and gain are specified by table below:



8316 Counter Read

This function is used to read the counter value of the Timer #0.



II6	Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error</i> <i>Codes</i> , for a code descriptions.
U16	Counter Value :	the counter value of the Timer #0
I16	Error Out :	Error code. <i>Error Out</i> describes the error status of this VI.

8316 Counter Start

The Timer #0 on the ACL-8316/12 can be freely programmed by the users. This function is used to program the Timer #0. This timer can be used as frequency generator if internal clock is used. It also can be used as event counter if external clock is used. All the 8254 mode is available.





Timer Mode :	the 8254 timer mode, the possible values are 1 through 5.
Counter Value :	the counter value of the Timer #0
Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error Codes</i> ,



for a code descriptions.



Error Out :

Error code. *Error Out* describes the error status of this VI.

8316 Counter Stop

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This VI is used to stop the timer operation. The timer is set to the 'One-shot' mode with counter value ' 0 '. That is, the clock output signal will be set to high after executing this function.

	Error In	831b Counter Value CRAR Error Out 5 Counter Stop
Il6	Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error Codes</i> , for a code descriptions.
U16	Counter Value :	the current counter value of the Timer #0
I16	Error Out :	Error code. <i>Error Out</i> describes the error status of this VI.

8316 DA Set Mode

This VI is used to configure the D/A output mode. There are four modes can be set ACL-8316/12 when the D/A output is used.



U8	DA Mode :	 transparency, binary data format transparency, 2's complement format double buffered, binary data format double buffered, 2's complement format
Il6	Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error Codes</i> , for a code descriptions.
I16	Error Out :	Error code. <i>Error Out</i> describes the error status of this VI.

8316 DA Write Channel

This VI is used to write data to D/A converters. There are two Digital-to-Analog conversion channels on the ACL-8316/12. The resolution of each channel is 12 bits; its data format can be binary or two's complement format. Which data format is used for this function, it is dependent on the VI 8316 DA Set Mode.





II6	Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error Codes</i> , for a code descriptions.
I16	Error Out :	Error code. <i>Error Out</i> describes the error status of this VI.

8316 DI Read Line

This VI is used to read data from digital input line (bit). There are 16 digital input lines on the ACL-8316/12. When performs this function, the digital input port is read and the value of the corresponding line is returned.

* line means each bit of digital input port.



U8	Line :	the DI line number, the value has to be set from 0 to 15.
I16	Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error Codes</i> , for a code descriptions.
I8	DI Data :	return value, either 0 or 1.

Error Out :

Error code. *Error Out* describes the error status of this VI.

8316 DI Read Port

This VI is used to read data from digital input port. There are 16bit digital inputs on the ACL-8316/12. The digital input status can be accessed by this function directly.

	Error In	DI Data DI Port Error Out 5 DI Read Port	
I16	Error In :	The error condition occurred before thi executes. This code default to no error an error has already occurred, this VI of not perform any operation. A value of 0 means no error, a negative value mean error. Refer to the Appendix, <i>Error Coo</i> for a code descriptions.	s VI : If does) ns an des ,
U16	DI Data :	return value from digital input port	
I16	Error Out :	Error code. <i>Error Out</i> describes the err status of this VI.	or

8316 DO Write Port

This VI is used to write data to digital output port. There are 16 digital outputs on the ACL-8316/12. You can control the digital outputs by this function directly.



U16	DO Data :	value will be written to digital output port
Il6	Error In :	The error condition occurred before this VI executes. This code default to no error. If an error has already occurred, this VI does not perform any operation. A value of 0 means no error, a negative value means an error. Refer to the Appendix, <i>Error Codes</i> , for a code descriptions.
I16	Error Out :	Error code. <i>Error Out</i> describes the error status of this VI.